

clirosoft | SOS

Elevate your Design Data Management for Hardware Design.

SPEED

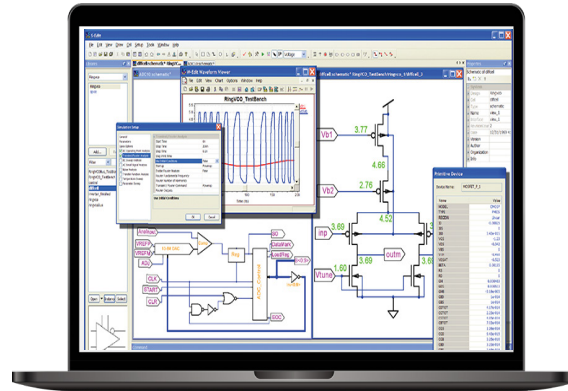
Experience a more rapid hardware development lifecycle so you can take on more projects.

SCALE

Hardware Configuration Management that adjusts to your growing analog, mixed-signal and digital design needs.

COLLABORATION

Collaborate on a multi-site design at speed and scale with an architecture specially built for hardware collaboration.



DESIGN DATA MANAGEMENT

Clirosoft SOS helps hardware design engineers manage hardware designs and projects from concept to tapeout. Unlike software engineers that use software configuration tools such as Git, Subversion, and Perforce to manage the large number of text files in software design, hardware engineers choose Clirosoft SOS to manage the large number of large design files in IC design.

OPTIMIZE NETWORK STORAGE

Clirosoft SOS optimizes network storage without proprietary hardware or operating/file system changes. Built for integrated circuit (IC) design where a design project typically contains thousands of very large files, Clirosoft SOS optimizes workspaces and uses cache servers to save disk space. Duplication is eliminated and only files being edited are placed as physical copies in the user workspace. Network storage is optimized and IT spending is reduced.

TOOL CONNECTORS

Clirosoft SOS integrates tightly with hardware design tools from all major EDA vendors such as Cadence, Synopsys, Mentor, Tanner and Keysight Technologies. For hardware designers using software configuration tools such as Git, Subversion and Perforce, Clirosoft SOS includes connectors to seamlessly integrate.

▶ WATCH VIDEO

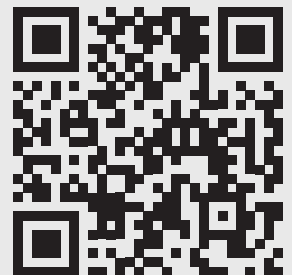
39500 Stevenson Pl., #100
Fremont, CA 94539
USA
+1 (510) 790 4732
info@clirosoft.com

f clirosoftinc

t ClirosoftInc

in clirosoft

clirosoft



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Cliosoft's data management collaboration platform has helped us manage design data from digital, analog, and RF teams at multiple sites, improving design team productivity as we develop high-performance CMOS RF and mixed-signal semiconductors for the cellular industry. The universal data management adaptor looks very promising as a way to manage our Agilent ADS data for upcoming projects.

ON Semiconductor®



Problems that Cliosoft SOS can solve for you

- Revision control and release management not directly available from the design tool of choice
- One cohesive system to manage all types of hardware designs
- Network disk usage optimization
- Access controls for the enterprise
- Composite design object support
- Collaborate on a multi-site hardware design at speed and scale

How Cliosoft SOS can help you

HARDWARE DESIGNER Create or update, release & notify IP and designs with complete control & management directly from your design tool.


HARDWARE INTEGRATOR Receive notifications of updated IP and designs to help guide selection, updates, and integration.


MANAGER Review changes, analyze conflicts and approve releases – easily.



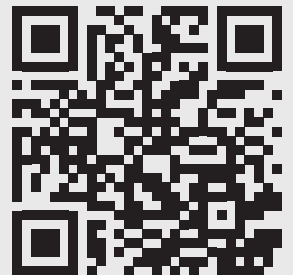
CONTACT US

 39500 Stevenson Pl., #100
Fremont, CA 94539
USA
 +1 (510) 790 4732
 info@cliosoft.com

 cliosoftinc

 CliosoftInc

 cliosoft



clio**soft**