

cliOSOFT | SOS

Elevate your Design Data Management for Hardware Design.

SPEED

Experience a more rapid hardware development lifecycle so you can take on more projects.

SCALE

Hardware Design Management that adjusts to your growing analog, mixed-signal, RF, and digital design needs.

COLLABORATION

Collaborate on a multi-site design at speed and scale with an architecture specially built for hardware collaboration.

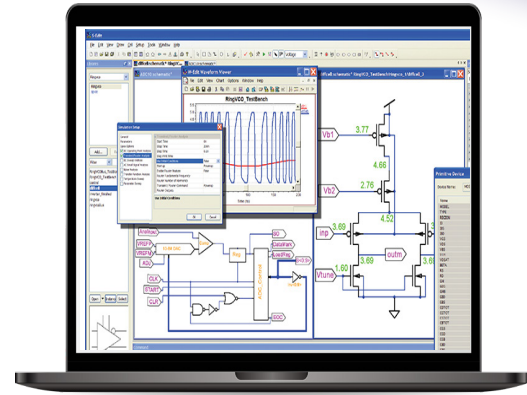
DESIGN DATA MANAGEMENT

Cliosoft SOS helps hardware design engineers manage hardware designs and projects from concept to tapeout.

Unlike software engineers that use software configuration tools such as Git, Subversion, and Perforce to manage the large number of text files in software design, hardware engineers choose Cliosoft SOS to manage the large number of large design files in IC design.

Cliosoft SOS helps you archive large files optimally such as GDS or simulation waveforms and is scalable, flexible, and secure.

Advanced revision control with release and derivative design management along with interfaces to issue tracking software make Cliosoft SOS unrivaled.



OPTIMIZE NETWORK STORAGE

Cliosoft SOS optimizes network storage without proprietary hardware or operating/file system changes.

Built for integrated circuit (IC) design where a design project typically contains thousands of very large files, Cliosoft SOS optimizes workspaces and uses cache servers to save disk space.

Duplication is eliminated and only files being edited are placed as physical copies in the user workspace. Network storage is optimized and IT spending is reduced.

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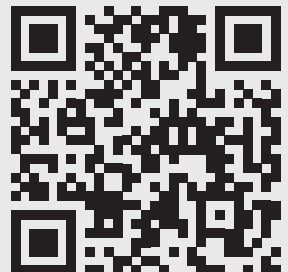
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Clirosoft's data management collaboration platform has helped us manage design data from digital, analog, and RF teams at multiple sites, improving design team productivity as we develop high-performance CMOS RF and mixed-signal semiconductors for the cellular industry. The universal data management adaptor looks very promising as a way to manage our Agilent ADS data for upcoming projects.

ON Semiconductor®



TOOL CONNECTORS

Clirosoft SOS integrates tightly with hardware design tools from all major EDA vendors such as Cadence, Synopsys, Siemens EDA, Keysight Technologies, and Silvaco.

For hardware designers using software configuration tools such as Git, Subversion and Perforce, Clirosoft SOS includes connectors to seamlessly integrate.

For chip design flows, Clirosoft SOS also integrates with Mathworks, Emphyrean, and Beanbag.

PROBLEMS THAT CLIROSOFT SOS CAN SOLVE FOR YOU

- Revision control and release management not directly available from the design tool of choice
- One cohesive system to manage all types of hardware designs
- Network disk usage optimization
- Access controls for the enterprise
- Composite design object support
- Collaborate on a multi-site hardware design at speed and scale
- Track the relationship between design issues and changes
- Review changes and analyze conflicts
- IP traceability

HOW CLIROSOFT SOS CAN HELP YOU

HARDWARE DESIGNER Create or update, release & notify IP and designs with complete control & management directly from your design tool.

HARDWARE INTEGRATOR Receive notifications of updated IP and designs to help guide selection, updates, and integration.

MANAGER Review changes, analyze conflicts and approve releases – easily.



CONTACT US

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